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Polyphase filter with integrators

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Polyphase filter with integrators

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(44)

The invention relates to a polyphase filter comprising at least two filters for filtering signals.

The invention also relates to an integrator for use in a polyphase filter comprising at least two filters for filtering signals, and to a receiver comprising a polyphase filter comprising at least two filters for filtering signals.

Such a polyphase filter is for example a two-phase filter comprising two filters for filtering two (input) signals, or a four-phase filter comprising four filters for filtering four (input) signals. Polyphase filters are used, for example, in television receivers, for example in the tuner just before the mixer.

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A prior art polyphase filter is known from US 3,559,042, published January 26, 1971, which discloses a polyphase network including N single phase circuits (filters), each circuit having a first impedance coupled between input and output terminals of said circuit, with said input terminal also being coupled to the output terminal of another circuit responding to an adjacent phase (leading or lagging) of the input signal by a second impedance having a different phase angle characteristic than that of the first impedance.

The known polyphase filter is disadvantageous, inter alia, due to, for example in case of being used in a tuner just before the mixer, needing buffers, said buffers limiting the dynamic range of the polyphase filter.

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It is an object of the invention, inter alia, of providing a polyphase filter as defined in the preamble which allows the dynamic range to be larger.

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The polyphase filter according to the invention is characterized in that said filters are coupled to integrators for integrating filtered signals.

The integrators coupled to said filters allow a larger dynamic range than the known combination of filters and buffers.

The invention is based upon an insight, inter alia, that a known polyphase filter with buffers has a limited dynamic range, and is based upon a basic idea, inter alia, that integrators replacing said buffers allow the dynamic range to be larger.

5 The invention solves the problem, inter alia, of providing a polyphase filter as defined in the preamble which allows the dynamic range to be larger, and introduces further advantageous functions, as described below.

According to a first embodiment of the polyphase filter according to the invention, an output of an integrator is coupled via a conductance element to an input of a previous integrator.

10 Said conductance element increases the selectivity of the polyphase filter. In case of a two-phase filter, generally there will be two filters each with an integrator. Then the output of the second integrator is coupled via a first conductance element to the input of the first integrator, and generally the output of the first integrator is coupled via a second conductance element to the input of the second integrator. In case of a four-phase filter,
15 generally there will be four filters each with an integrator. The output of the second integrator is coupled via a first conductance element to the input of the first integrator, and generally the output of the third integrator is coupled via a second conductance element to the input of the second integrator, etc.

20 According to a second embodiment of the polyphase filter according to the invention an output of an integrator is coupled via a capacitor to an input of a next integrator.

Said capacitor further increases the selectivity of the polyphase filter. In case of a two-phase filter, the output of the first integrator is coupled via a first capacitor to the input of the second integrator, and generally the output of the second integrator is coupled via a second capacitor to the input of the first integrator. In case of a four-phase filter, the output
25 of the first integrator is coupled via a first capacitor to the input of the second integrator, and generally the output of the second integrator is coupled via a second capacitor to the input of the third integrator, etc. Said capacitor improves the quality factor of the polyphase filter, allowing a quality factor larger than $\frac{1}{2}\sqrt{2}$, without making a loop-amplification larger than one in a three-phase, four-phase, five-phase etc. filter.

30 According to a third embodiment of the polyphase filter according to the invention an integrator comprises an amplifier with an admittance element in a feedback path.

Said integrator is provided with an amplifying function and can be integrated well in an integrated circuit, with said admittance element defining an integration factor.

According to a fourth embodiment of the polyphase filter according to the invention a filter comprises a passive element and an amplifier comprises an operational amplifier.

5 This polyphase filter based on filters each comprising passive elements and operational amplifiers can be integrated well in an integrated circuit, with each part of said fourth embodiment being a sub-embodiment possibly to be used in combination with one or more other sub-embodiments or not.

According to a fifth embodiment of the polyphase filter according to the invention a passive element comprises a resistor and a capacitor and an admittance element
10 comprises a capacitor and a conductance element coupled in parallel to each other.

This polyphase filter based upon resistors and capacitors and admittance elements comprising capacitors and conductance elements coupled in parallel to each other can be integrated well in an integrated circuit, with each part of said fifth embodiment being a sub-embodiment possibly to be used in combination with one or more other sub-
15 embodiments or not.

According to a sixth embodiment of the polyphase filter according to the invention said polyphase filter comprises at least one signal inversion between integrators.

Said signal inversion allows a conductance element to have a negative value necessary for locating at least one pole at the most optimal location in the plane pole-zero
20 plot. A signal inversion may be realized by exchanging a pair of connections in an integrated circuit (exchanging plus and minus) in case of a balanced polyphase filter or may be realized by an inverter for example comprising an operational amplifier.

It should be noted that the words "previous" and "next" should not be looked at too narrowly and that these words may correspond with "neighboring". Between an
25 integrator and a previous/next/neighboring integrator, there could be located further integrators or not. Usually, but not exclusively, said integrators will be coupled more or less symmetrically.

Embodiments of the integrator according to the invention and of the receiver according to the invention correspond with the embodiments of the polyphase filter according
30 to the invention.

These and other aspects of the invention will be apparent from and elucidated with reference to the embodiments(s) described hereinafter.

Fig. 1 illustrates in block diagram form a polyphase filter according to the invention comprising four filters and four integrators according to the invention, and

Fig. 2 illustrates in block diagram form a polyphase filter according to the invention comprising two filters and two integrators according to the invention with an
5 inverter.

The polyphase filter shown in fig. 1 comprises four filters 1-4. Filter 1 (2,3,4) comprises a capacitor 10 (20,30,40) located between an input and an output of filter 1 (2,3,4),
10 with a resistor 11 (21,31,41) being coupled to the input of filter 1 (2,3,4) and to an output of filter 2 (3,4,1). The output of filter 1 (2,3,4) is coupled to a first input of an operational amplifier 12 (22,32,42), of which a second input is coupled to ground and of which an output is coupled via an admittance element 13 (23,33,43) to said first input. Said output is further coupled via a conductance element 44 (14,24,34) to an input of an operational amplifier 42
15 (12,22,32), and is further coupled via a capacitor 25 (35,45,15) to an input of an operational amplifier 22 (32,42,12).

The output of first (second, third, fourth) filter 1 (2,3,4) is coupled to an input of a first (second, third, fourth) integrator 12-13 (22-23,32-33,42-43) comprising an operational amplifier 12 (22,32,42) with an admittance element 13 (23,33,43) in a feedback
20 path. Other amplifiers like for example a single or a few transistors are however not to be excluded. Said admittance element 13 (23,33,43) for example comprises a capacitor and a conductance element coupled in parallel to each other, but other and/or further admittance elements belong to the possibilities.

The output of first (second, third, fourth) integrator 12-13 (22-23,32-33,42-43)
25 is coupled via a conductance element 44 (14,24,34) to an input of a previous integrator 42-43 (12-13,22-23,32-33), with further elements not to be excluded.

The output of first (second, third, fourth) integrator 12-13 (22-23,32-33,42-43) is coupled via a capacitor 25 (35,45,15) to an input of a next integrator
22-23 (32-33,42-43,12-13), with further elements not to be excluded.

30 The first (second, third, fourth) filter 1 (2,3,4) comprises passive elements like said capacitor 10 (20,30,40) and said resistor 11 (21,31,41), with other and/or further passive elements not to be excluded. Usually, at least two passive elements in each filter 1,2,3,4 will have a different phase angle characteristic.

Alternatively, instead of a filter 1 (2,3,4) comprising passive elements

10-11 (20-21,30-31,40-41) and being coupled to an integrator 12-13 (22-23,32-33,42-43), a filter 1 (2,3,4) may comprise passive elements 10-11 (20-21,30-31,40-41) and may further comprise an integrator 12-13 (22-23,32-33,42-43) and/or a conductance element (thereby having the option of including either the one coupled to the previous integrator or the one coupled to the next integrator) and/or a capacitor (and thereby again having the option of including either the one coupled to the previous integrator or the one coupled to the next integrator).

Amplifiers with admittance elements in feedback paths introduce poles on the negative real axis in the plane pole-zero plot. The conductance elements introduce frequency shifts of said poles in the plane pole-zero plot. The capacitors also give frequency shifts of said poles in the plane pole-zero plot. Quality factors larger than $\frac{1}{2}\sqrt{2}$ in a four-phase filter now belong to the possibilities.

The polyphase filter shown in fig. 2 comprises a first (second) operational amplifier 50 (55) of which a first input is coupled to an admittance element 52 (57) and of which a second input is coupled to ground and of which an output is coupled via an admittance element 51 (56) to said first input. Another side of admittance element 52 (57) forms a first (second) input of this polyphase filter. Said output of first (second) operational amplifier 50 (55) forms a first (second) output of this polyphase filter.

Said first input of this polyphase filter is further coupled to a conductance element 58. Another side of the conductance element 58 is coupled to a first input of a third operational amplifier 60. A second input of the third operational amplifier 60 is coupled to ground. An output of the third operational amplifier 60 is coupled via a conductance element 61 to said first input of third operational amplifier 60 and via a conductance element 62 to said first input of second operational amplifier 55. Said first input of third operational amplifier 60 is further coupled via a conductance element 59 to the output of first operational amplifier 50.

Said second input of this polyphase filter is further coupled to a conductance element 53, of which an other side is coupled to said first input of first operational amplifier 50 and via a conductance element 54 to said output of second operational amplifier 55.

Admittance element 52 and conductance element 58 form a first filter, with operational amplifier 50 with admittance element 51 forming a first integrator. Admittance element 57 and conductance element 53 form a second filter, with operational amplifier 55 with admittance element 56 forming a second integrator. Conductance element 54 (59) couples an output of a second (first) integrator to an input of a first (second) integrator.

Third operational amplifier 60 together with conductance elements 61 and 62 forms an inverter located between both integrators and allows conductance elements 58 and 59 to have a negative value necessary for locating at least one pole at the most optimal location in the plane pole-zero plot.

5 With respect alternatives and/or additions to said operational amplifiers, admittance elements and conductance elements, see above.

Let us consider that the admittance elements 51 and 56 are $Y_b = sC_b + G_{br}$, admittance elements 52 and 57 are $Y_a = sC_a + G_{ar}$, conductance element 53 is G_a and conductance element 58 is $-G_a$, conductance element 54 being G_b and conductance element 59 being $-G_b$. Said

10 negative values are realized via said inverter. The transfer function becomes:

$$H_p(s) = - (sC_a + G_{ar} + jG_a) / (sC_b + G_{br} + jG_b),$$

and a pole can be found at $s_x = - (G_{br} + jG_b) / C_b$,

and a zero can be found at $s_o = - (G_{ar} + jG_a) / C_a$.

It is remarked that the scope of protection of the invention is not restricted to
 15 the embodiments described herein. Neither is the scope of protection of the invention restricted by the reference numerals in the claims. The word 'comprising' does not exclude other parts than those mentioned in the claims. The word 'a(n)' preceding an element does not exclude a plurality of those elements. Means forming part of the invention may both be implemented in the form of dedicated hardware or in the form of a programmed purpose
 20 processor. The invention resides in each new feature or combination of features.

CLAIMS:

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(44)

1. Polyphase filter comprising at least two filters for filtering signals, characterized in that said filters are coupled to integrators for integrating filtered signals.
2. Polyphase filter according to claim 1, wherein an output of an integrator is
5 coupled via a conductance element to an input of a previous integrator.
3. Polyphase filter according to claim 2, wherein an output of an integrator is coupled via a capacitor to an input of a next integrator.
- 10 4. Polyphase filter according to claim 3, wherein an integrator comprises an amplifier with an admittance element in a feedback path.
5. Polyphase filter according to claim 4, wherein a filter comprises a passive element and wherein an amplifier comprises an operational amplifier.
15
6. Polyphase filter according to claim 5, wherein a passive element comprises a resistor and a capacitor and wherein an admittance element comprises a capacitor and a conductance element coupled in parallel to each other.
- 20 7. Polyphase filter according to claim 6, wherein said polyphase filter comprises at least one signal inversion between integrators.
8. Integrator for use in a polyphase filter comprising at least two filters for filtering signals, wherein said filters are coupled to integrators for integrating filtered signals.
- 25 9. Receiver comprising a polyphase filter comprising at least two filters for filtering signals, wherein said filters are coupled to integrators for integrating filtered signals.

ABSTRACT:

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(44)

Polyphase filters comprising filters each filter comprising passive elements. The filters are provided with integrators comprising amplifiers with admittance elements in feedback paths to create one or more other poles not situated on the negative imaginary axis in the plane pole-zero plot. A conductance element couples an output of an integrator to an input of a previous integrator for introducing a frequency shift for at least one pole in the plane pole-zero plot. A capacitor couples an output of an integrator to an input of a next integrator for improving the quality factor of the polyphase filter, which factor can then exceed $\frac{1}{2}\sqrt{2}$. A signal inversion allows conductance elements to have negative values necessary for locating at least one pole at the most optimal location in the plane pole-zero plot.

Fig. 1

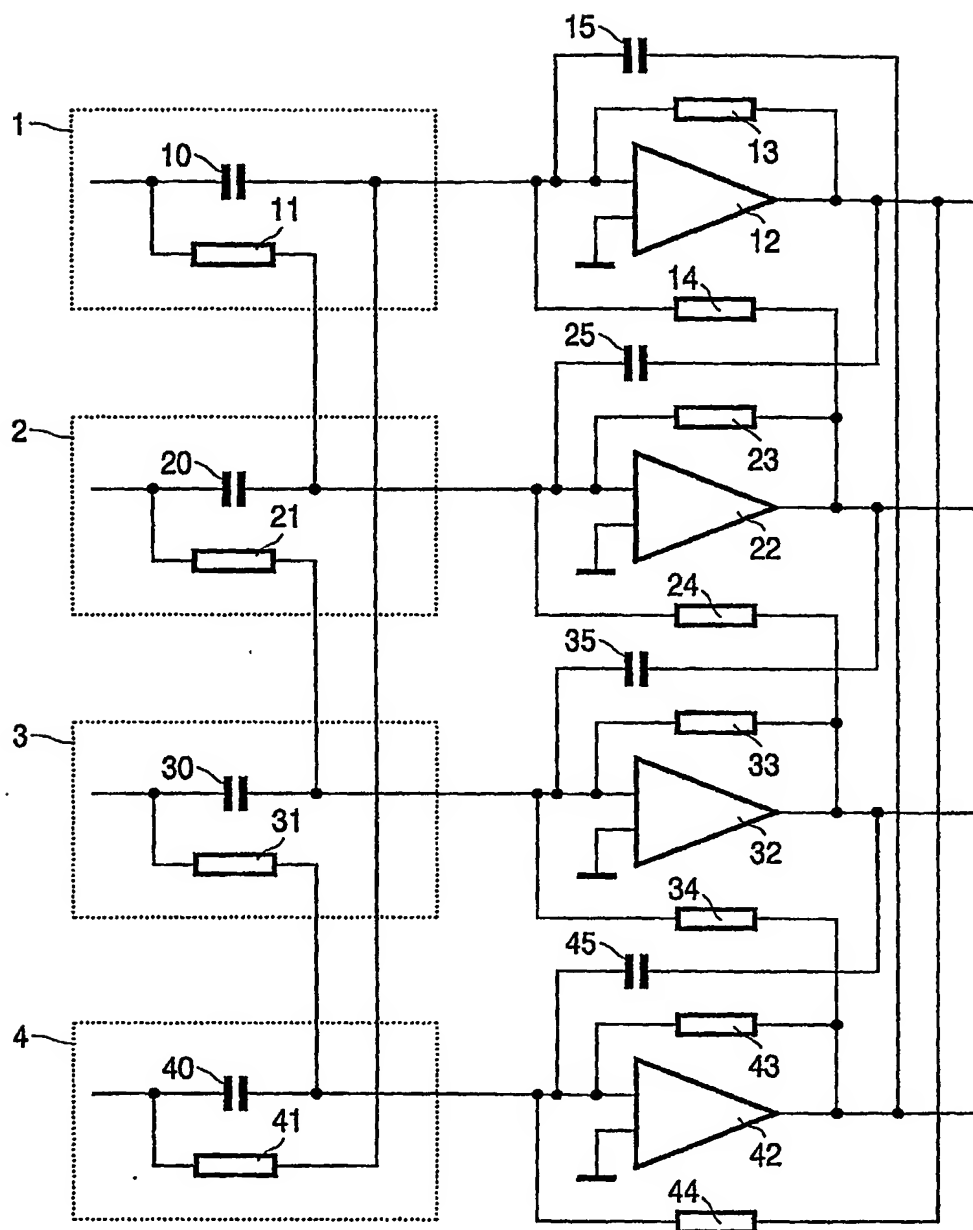


FIG. 1

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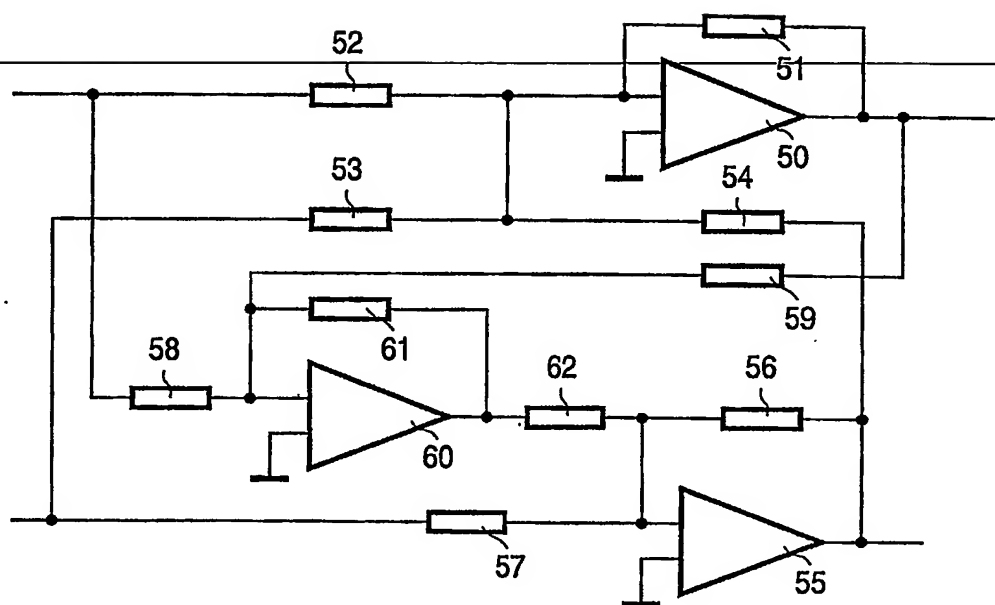


FIG. 2